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B	<div><div><table><tr><th>Layer</th><th>Name</th><th>Material</th><th>Thickness</th><th>Constant</th><th>Board Layer Stack</th></tr><tr><td></td><td>Top Overlay</td><td></td><td></td><td></td><td></td></tr><tr><td></td><td>Top Solder</td><td>Solder Resist</td><td>0.40mil</td><td>3.5</td><td></td></tr><tr><td>1</td><td>Top Layer</td><td></td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Dielectric 1</td><td>PP-006</td><td>8.00mil</td><td>4.1</td><td></td></tr><tr><td>2</td><td>Signal Layer 1</td><td>CF-004</td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Dielectric 2</td><td>PP-006</td><td>8.00mil</td><td>4.1</td><td></td></tr><tr><td>3</td><td>Signal Layer 2</td><td>CF-004</td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Dielectric3</td><td>FR-4 High Tg</td><td>12.00mil</td><td>4.8</td><td></td></tr><tr><td>4</td><td>Signal Layer 3</td><td>CF-004</td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Dielectric 4</td><td>PP-006</td><td>8.00mil</td><td>4.1</td><td></td></tr><tr><td>5</td><td>Signal Layer 4</td><td>CF-004</td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Dielectric 5</td><td>PP-006</td><td>8.00mil</td><td>4.1</td><td></td></tr><tr><td>6</td><td>Bottom Layer</td><td></td><td>2.76mil</td><td></td><td></td></tr><tr><td></td><td>Bottom Solder</td><td>Solder Resist</td><td>0.40mil</td><td>3.5</td><td></td></tr><tr><td></td><td>Bottom Overlay</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="3">Total board thickness:</td><td colspan="3">61.34mil</td></tr></table></div><div><table><tr><th>Symbol</th><th>Quantity</th><th>Finished Hole Size</th><th>Plated</th><th>Hole Type</th><th>Drill Layer Pair</th><th>Hole Tolerance</th></tr><tr><td>⊠</td><td>2</td><td>204.72mil (5.200mm)</td><td>NPTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>✱</td><td>18</td><td>7.87mil (0.200mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>⊕</td><td>277</td><td>10.00mil (0.254mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>▽</td><td>12</td><td>40.00mil (1.016mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>■</td><td>2</td><td>55.12mil (1.400mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>○</td><td>16</td><td>72.84mil (1.850mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td>□</td><td>4</td><td>125.98mil (3.200mm)</td><td>PTH</td><td>Round</td><td>Top Layer - Bottom Layer</td><td></td></tr><tr><td colspan="2">331 Total</td><td></td><td></td><td></td><td></td><td></td></tr></table></div><div><div><div></div><div>1000.00mil</div></div></div></div>						Layer	Name	Material	Thickness	Constant	Board Layer Stack		Top Overlay						Top Solder	Solder Resist	0.40mil	3.5		1	Top Layer		2.76mil				Dielectric 1	PP-006	8.00mil	4.1		2	Signal Layer 1	CF-004	2.76mil				Dielectric 2	PP-006	8.00mil	4.1		3	Signal Layer 2	CF-004	2.76mil				Dielectric3	FR-4 High Tg	12.00mil	4.8		4	Signal Layer 3	CF-004	2.76mil				Dielectric 4	PP-006	8.00mil	4.1		5	Signal Layer 4	CF-004	2.76mil				Dielectric 5	PP-006	8.00mil	4.1		6	Bottom Layer		2.76mil				Bottom Solder	Solder Resist	0.40mil	3.5			Bottom Overlay					Total board thickness:			61.34mil			Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance	⊠	2	204.72mil (5.200mm)	NPTH	Round	Top Layer - Bottom Layer		✱	18	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer		⊕	277	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer		▽	12	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer		■	2	55.12mil (1.400mm)	PTH	Round	Top Layer - Bottom Layer		○	16	72.84mil (1.850mm)	PTH	Round	Top Layer - Bottom Layer		□	4	125.98mil (3.200mm)	PTH	Round	Top Layer - Bottom Layer		331 Total							
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C	<div><div><div><div><div>ALL ARTWORK VIEWED FROM TOP SIDE</div><div>LAYER NAME = PCB Fabrication</div><div>PLOT NAME = Fabrication Drawing</div></div></div><div><div>BOARD #: SR148</div><div>TID #: N/A</div><div>GENERATED : 10/14/2025 12:28:23 PM</div></div><div><div>REV: B</div><div>SUN REV: Not in version control</div><div>TEXAS INSTRUMENTS</div></div></div><div><div>Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div><div><div>ENGINEER: T Hegarty</div><div>LAYOUT BY: M.Figueroa</div></div><div><div>SCALE: 1.00</div><div>ALTIM DESIGNER VERSION: 25.8.1.18</div></div></div></div>																																																																																																																																																																											
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